

TOSHIBA PHOTOCOUPLER GaAlAs IRED & PHOTO-IC

TLP114A

DIGITAL LOGIC ISOLATION.

LINE RECEIVER.

POWER SUPPLY CONTROL FEEDBACK CONTROL.

SWITCHING POWER SUPPLY.

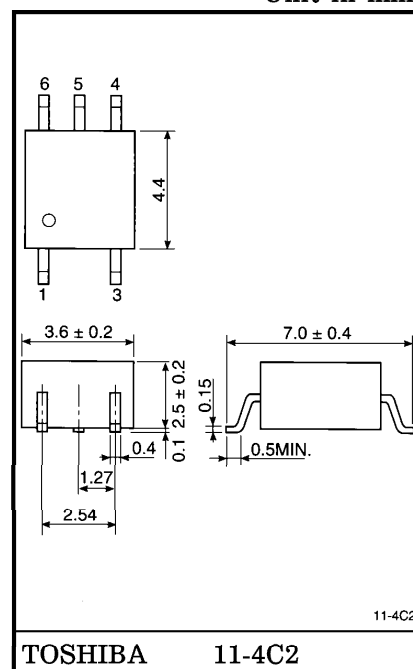
TRANSISTOR INVERTOR.

The TOSHIBA MINI FLAT COUPLER TLP114A is a small outline coupler, suitable for surface mount assembly.

TLP114A consists of a high output power GaAlAs light emitting diode, optically coupled to a high speed detector of one chip photodiode-transistor.

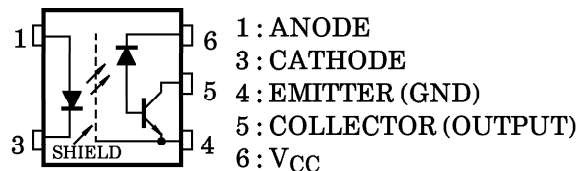
- Isolation Voltage : 3750 Vrms (Min.)
- Switching Speed : $t_{pHL}=0.8\mu s$, $t_{pLH}=0.8\mu s$, (Max.)
($R_L=1.9k\Omega$)
- TTL Compatible
- UL Recognized : UL1577, File No. E67349

Unit in mm

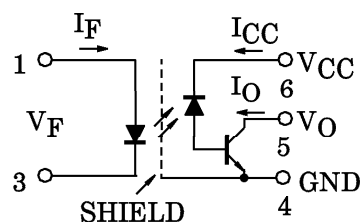


Weight : 0.09g

PIN CONFIGURATION (TOP VIEW)



SCHEMATIC



MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
L D E	Forward Current (Note 1)	I _F	20	mA
	Pulse Forward Current (Note 2)	I _{FP}	40	mA
	Peak Transient Forward Current (Note 3)	I _{FPT}	1	A
	Reverse Voltage	V _R	5	V
D E T E C T O R	Output Current	I _O	8	mA
	Peak Output Current	I _{OP}	16	mA
	Supply Voltage	V _{CC}	−0.5~30	V
	Output Voltage	V _O	−0.5~20	V
	Output Power Dissipation (Note 4)	P _O	100	mW
Operating Temperature Range		T _{opr}	−55~100	°C
Storage Temperature Range		T _{stg}	−55~125	°C
Lead Solder Temperature (10 sec.)		T _{sol}	260	°C
Isolation Voltage (AC, 1 min., R.H. ≤ 60%) (Note 5)		BV _S	3750	V _{rms}

(Note 1) Derate 0.36mA/°C above 70°C.

(Note 2) 50% duty cycle, 1ms pulse width.

Derate 0.72mA/°C above 70°C.

(Note 3) Pulse width ≤ 1μs, 300pps.

(Note 4) Derate 1.8mW/°C above 70°C.

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
LDE	Forward Voltage	V_F	$I_F = 16\text{mA}$	1.22	1.42	1.72	V
	Forward Voltage Temperature Coefficient	$\Delta V_F / \Delta T_a$	$I_F = 16\text{mA}$	—	—2	—	mV / °C
	Reverse Current	I_R	$V_R = 3\text{V}$	—	—	10	μA
	Capacitance Between Terminals	C_T	$V_F = 0, f = 1\text{MHz}$	—	30	—	pF
DETECTOR	High Level Output Current	$I_{OH(1)}$	$I_F = 0\text{mA}, V_{CC} = V_O = 5.5\text{V}$	—	3	500	nA
		$I_{OH(2)}$	$I_F = 0\text{mA}, V_{CC} = 30\text{V}$ $V_O = 20\text{V}$	—	—	5	μA
		I_{OH}	$I_F = 0\text{mA}, V_{CC} = 30\text{V}$ $V_O = 20\text{V}, T_a = 70^\circ\text{C}$	—	—	50	
	High Level Supply Current	I_{CCH}	$I_F = 0\text{mA}, V_{CC} = 30\text{V}$	—	0.01	1	μA
COUPLED	Current Transfer Ratio	I_O / I_F	$I_F = 16\text{mA}, V_{CC} = 4.5\text{V}$ $V_O = 0.4\text{V}$	20	—	—	%
	Low Level Output Voltage	V_{OL}	$I_F = 16\text{mA}, V_{CC} = 4.5\text{V}$ $I_O = 2.4\text{mA}$	—	—	0.4	V
	Isolation Resistance	R_S	R.H. $\leq 60\%$, $V_S = 500\text{V}$ (Note 5)	5×10^{10}	10^{14}	—	Ω
	Stray Capacitance Between Input to Output	C_S	$V_S = 0, f = 1\text{MHz}$ (Note 5)	—	0.8	—	pF

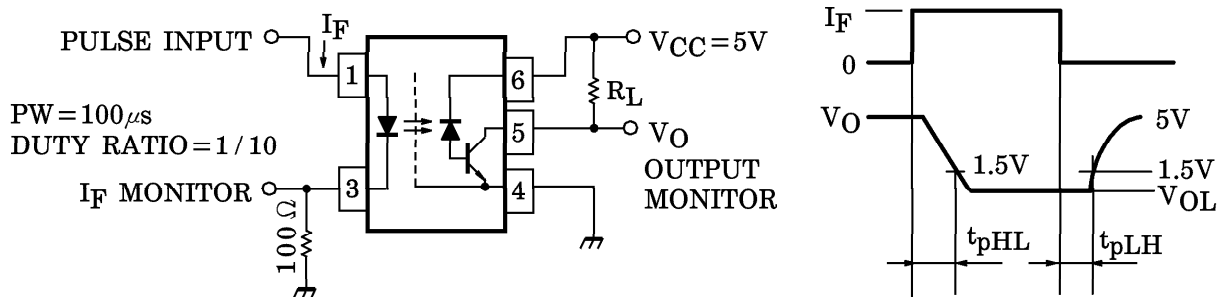
SWITCHING CHARACTERISTICS (Ta = 25°C, VCC = 5V)

CHARACTERISTIC	SYMBOL	TEST CIR - CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Propagation Delay Time (H→L)	t_{pHL}	1	$I_F = 0 \rightarrow 16\text{mA}$ $V_{CC} = 5\text{V}, R_L = 1.9\text{k}\Omega$	—	—	0.8	μs
Propagation Delay Time (L→H)	t_{pLH}	1	$I_F = 16 \rightarrow 0\text{mA}$ $V_{CC} = 5\text{V}, R_L = 1.9\text{k}\Omega$	—	—	0.8	μs
Common Mode Transient Immunity at High Output Level	C_{MH}	2	$I_F = 0\text{mA}$ $V_{CM} = 400\text{Vp-p}$ $R_L = 4.1\text{k}\Omega$	5000	10000	—	V / μs
Common Mode Transient Immunity at Low Output Level	C_{ML}	2	$I_F = 16\text{mA}$ $V_{CM} = 400\text{Vp-p}$ $R_L = 4.1\text{k}\Omega$	—5000	—10000	—	V / μs

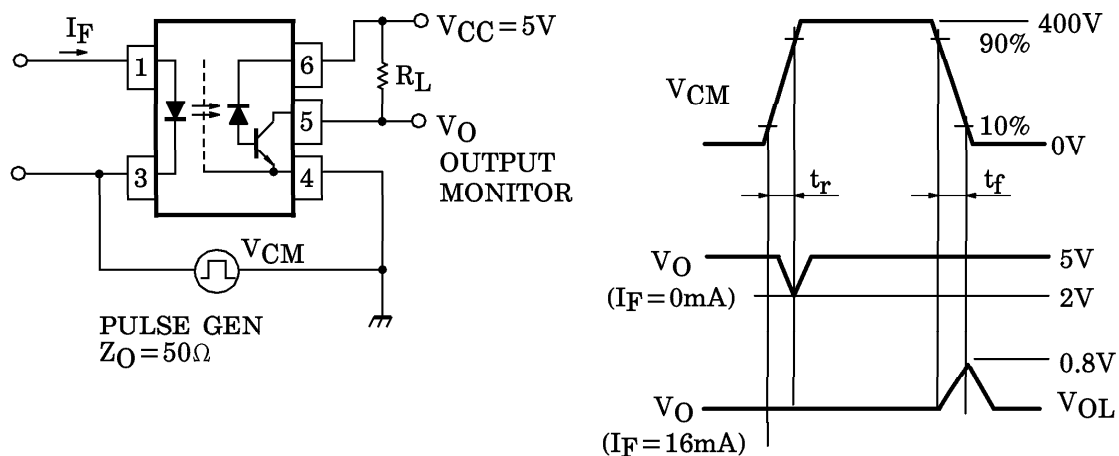
(Note 5) Device considered a two-terminal device : Pins 1 and 3 shorted together, and pins 4, 5 and 6 shorted together.

(Note 6) Maximum electrostatic discharge voltage for any pins : 100V (C=200pF, R=0)

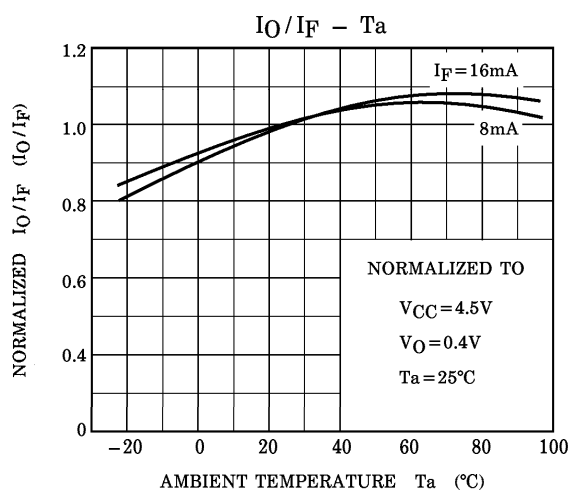
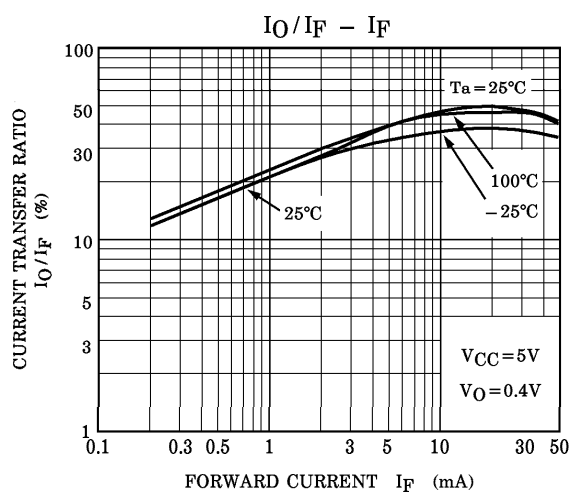
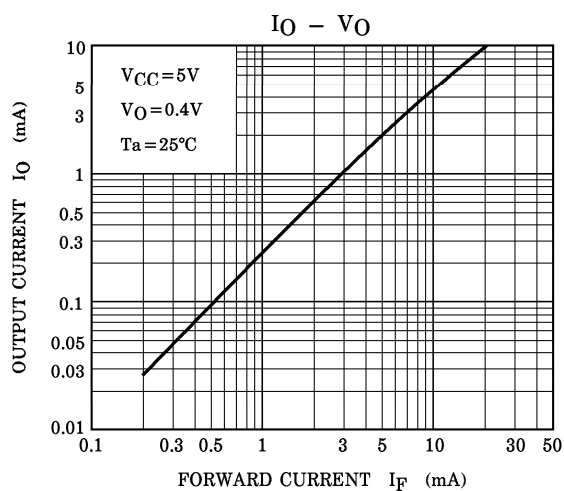
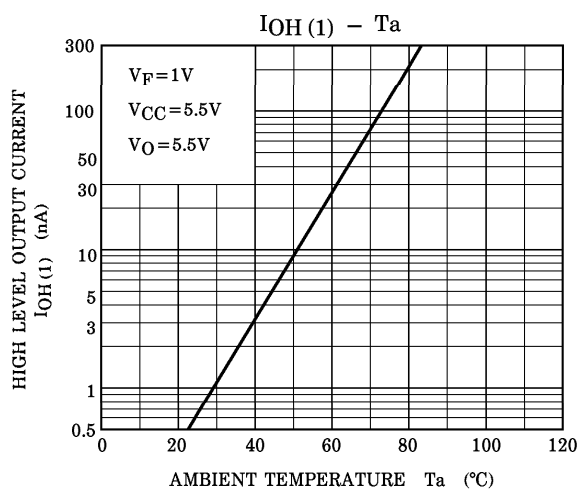
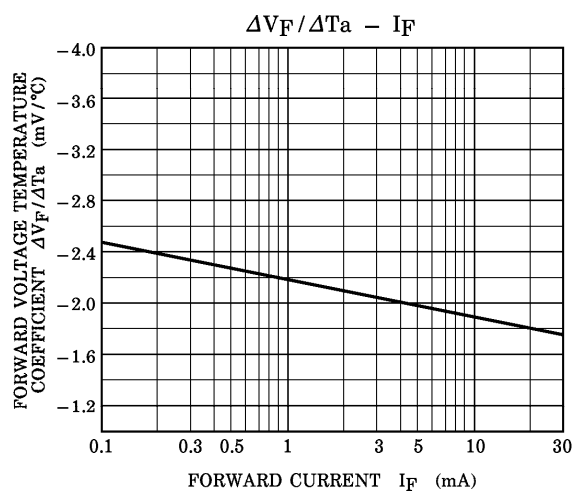
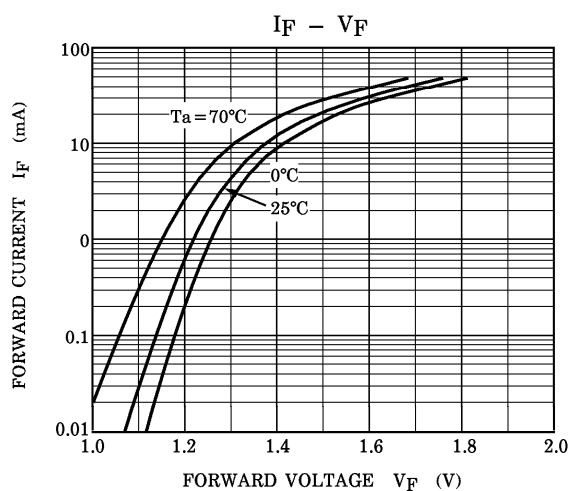
TEST CIRCUIT 1 : Switching Time Test Circuit

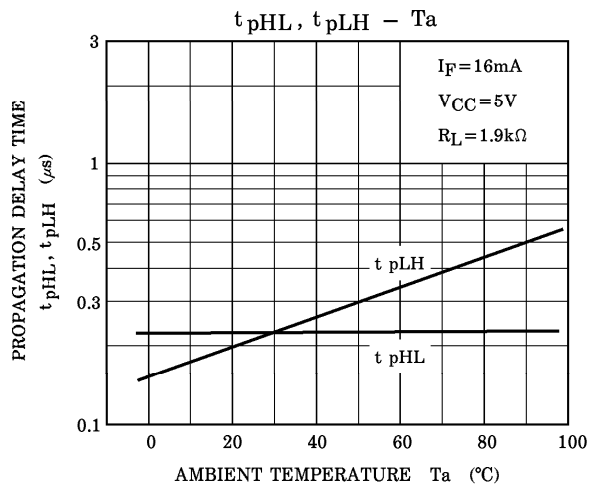
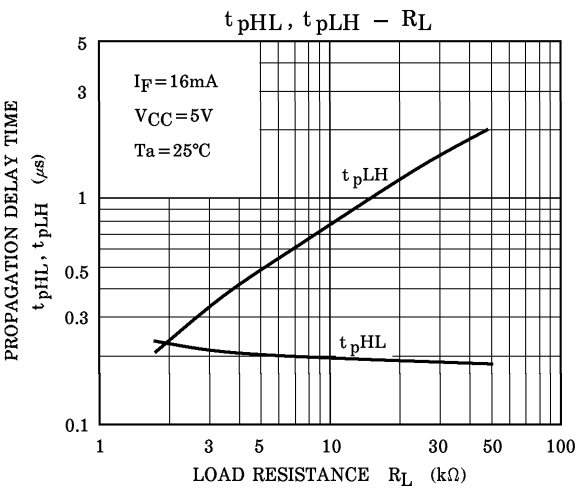
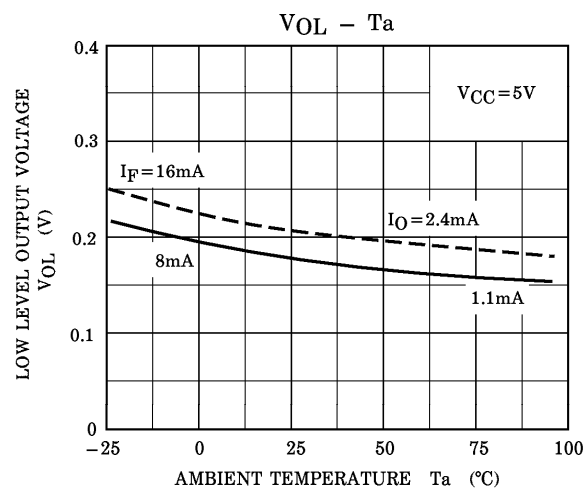
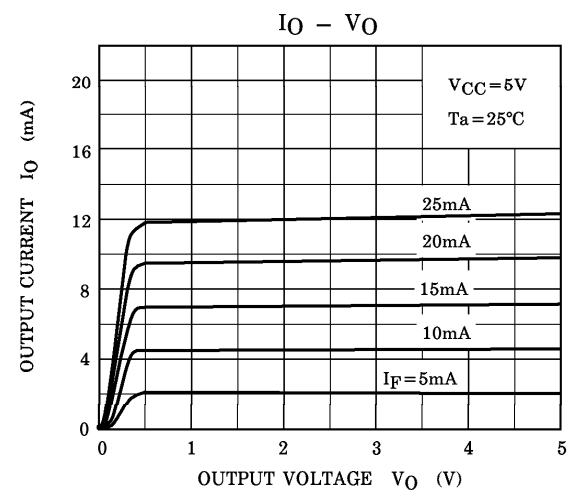


TEST CIRCUIT 2 : Common Mode Transient Immunity Test Circuit



$$CM_H = \frac{320 (V)}{t_r (\mu s)} , \quad CM_L = \frac{320 (V)}{t_r (\mu s)}$$





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