

DC-AC INVERTER UNIT

PS-M06D05S5-NJ1L-152(S) (6 WATT DUAL OUTPUTS)

(MODIFICATION)

DESCRIPTION :

This low profile DC to AC Inverter is developed for dual lamps.
Optimized for **Sharp LQ104V1DG51**

APPLICABLE LCD:

6,5 inches double lamp type
Lamp Voltage 467Vrms
Lamp Current 6mArms
Lamp Start Up Voltage 1500Vrms (Vin : 5V)



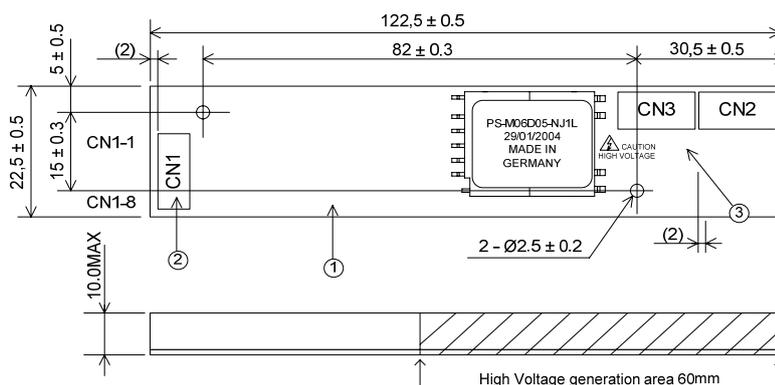
FEATURES :

- Remote ON/OFF
- Current Feedback Circuit Control
- RoHS compliant (S)

TEMPERATURE & HUMIDITY :

Operating Temperature Range -10° C~ +60° C
Storage Temperature Range -20° C~ +85° C
Humidity 95 %RH max

DIMENSIONS : L x W x H **123 x 23 x 10 mm**



Unit : mm
Weight :20 (g) typ.

Components

No.	Part Description	Qty.	Material	Note
1	PCB	1	UL94V-0	t=1.6mm
2	Connector CN1	1	S5B-PH-SM3	JST or equal
3	Connector CN2 + CN3	2	SM02(8.0)B-BHS-1-TB	JST or equal

Input side CN1:

Pin No.	Symbols	Ratings
CN 1-1	Vin	5 Vdc
CN 1-2	GND	
CN 1-3	Ext. PWM	0 ~ 5 Vdc (L activ)
CN 1-4	N.C.	
CN 1-5	Vrmt	0 / 5 Vdc (H activ)

Output side CN2 + CN3:

Pin No.	Symbols	Ratings
CN 3-1	VHIGH2	467Vrms (6.0m Arms)
CN 3-2	N.C.	-
CN 2-3	VLOW	0Vdc

Power Systems – The Power Solution

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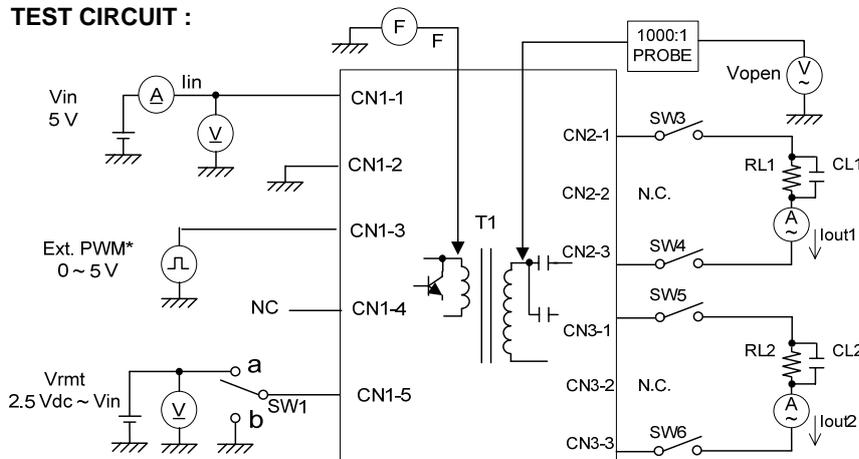
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ELECTRICAL CHARACTERISTICS :

Parameters	Symbols	Conditions			Specification			Unit	Note
		Vin (V)	Vrmt (V)	Tu (°C)	Min.	Typ.	Max.		
Output Current	Iout	5±0.05	5±0.25	-10 ~ +60	5.5	6.0	6.5	mArms	Duty 100%
Output Current	Iout	5±0.5	5±0.25	-10 ~ +60	5.0	6.0	7.0	mArms	
Input Current	Iin	5±0.5	5±0.25	-10 ~ +60	-	1.6	2.0	Adc	
Frequency	F	5±0.5	5±0.25	-10 ~ +60	50	60	70	kHz	
Open Circuit Voltage	Vopen	4.75	5±0.25	-10 ~ +60	1.300	1.500	-	Vrms	

- Note 1 : Please keep minimum of 2mm clearance (all directions) between inverter high voltage area as marked on mechanical drawing and any conductors.
- Note 2: Remote ON/OFF (CN1-5) has priority and can not be used in combination with External PWM Dimming (CN1-3). When 'Ext. PWM' (CN1-3) is used 'Vrmt' (CN1-5) should not be connected.
- Note 3: The external PWM frequency should be in a range of 100 ~ 500 Hz. For other frequency please request. The optimum is 250 Hz. Here you will normally not get a interference to the vertical sync. frequency.

TEST CIRCUIT :



SW1	Operation of unit
a	Operation
b	Non operation

*) Only active when Vrmt is 0 Vdc or floating

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