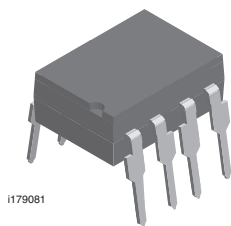
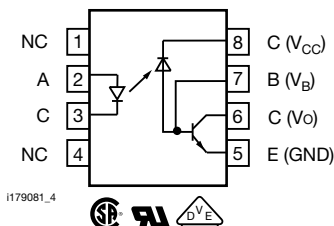


High Speed Optocoupler, 1 MBd, Photodiode with Transistor Output



i179081



i179081_4



FEATURES

- Isolation test voltages: 5300 V_{RMS}
- TTL compatible
- High bit rates: 1 Mbit/s
- High common-mode interference immunity
- Bandwidth 2 MHz
- Open-collector output
- External base wiring possible
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC


RoHS
COMPLIANT

DESCRIPTION

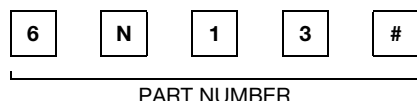
The 6N135 and 6N136 are optocouplers with a GaAlAs infrared emitting diode, optically coupled with an integrated photo detector which consists of a photo diode and a high-speed transistor in a DIP-8 plastic package.

Signals can be transmitted between two electrically separated circuits up to frequencies of 2 MHz. The potential difference between the circuits to be coupled should not exceed the maximum permissible reference voltages.

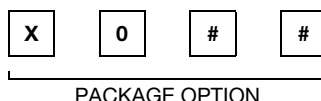
AGENCY APPROVALS

- UL1577, file no. E52744 system code H double protection
- DIN EN 60747-5-2 (VDE0884)/DIN EN 60747-5-5 (pending), available with option 1
- CSA 93751

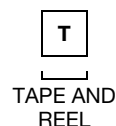
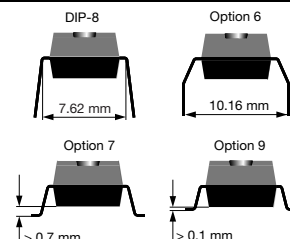
ORDERING INFORMATION



PART NUMBER



PACKAGE OPTION


TAPE AND
REEL


AGENCY CERTIFIED/PACKAGE	CTR (%)	
	≥ 7	≥ 19
UL, CSA		
DIP-8	6N135	6N136
DIP-8, 400 mil, option 6	-	6N136-X006
SMD-8, option 7	6N135-X007T ⁽¹⁾	6N136-X007T ⁽¹⁾
SMD-8, option 9	-	6N136-X009T ⁽¹⁾
VDE, UL, CSA	≥ 7	≥ 19
DIP-8	-	6N136-X001
DIP-8, 400 mil, option 6	-	6N136-X016
SMD-8, option 7	6N135-X017T ⁽¹⁾	6N136-X019T

Note

⁽¹⁾ Also available in tubes; do not add T to end

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	5	V
Forward current		I _F	25	mA
Peak forward current	t = 1 ms, duty cycle 50 %	I _{FSM}	50	mA
Maximum surge forward current	t ≤ 1 μs, 300 pulses/s		1	A
Thermal resistance		R _{th}	700	K/W
Power dissipation	T _{amb} = 70 °C	P _{diss}	45	mW



ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
OUTPUT				
Supply voltage		V_S	- 0.5 to 15	V
Output voltage		V_O	- 0.5 to 15	V
Emitter base voltage		V_{EBO}	5	V
Output current		I_O	8	mA
Maximum output current			16	mA
Base current		I_B	5	mA
Thermal resistance			300	K/W
Power dissipation	$T_{amb} = 70\text{ }^{\circ}\text{C}$	P_{diss}	100	mW
COUPLER				
Isolation test voltage between emitter and detector	$t = 1\text{ s}$	V_{ISO}	5300	V_{RMS}
Pollution degree (DIN VDE 0109)			2	
Isolation resistance	$V_{IO} = 500\text{ V}$, $T_{amb} = 25\text{ }^{\circ}\text{C}$	R_{IO}	$\geq 10^{12}$	Ω
	$V_{IO} = 500\text{ V}$, $T_{amb} = 100\text{ }^{\circ}\text{C}$	R_{IO}	$\geq 10^{11}$	Ω
Storage temperature range		T_{stg}	- 55 to + 125	$^{\circ}\text{C}$
Ambient temperature range		T_{amb}	- 55 to + 100	$^{\circ}\text{C}$
Soldering temperature ⁽¹⁾	max. $\leq 10\text{ s}$, dip soldering $\geq 0.5\text{ mm}$ from case bottom	T_{sld}	260	$^{\circ}\text{C}$

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 16\text{ mA}$		V_F		1.33	1.9	V
Breakdown voltage	$I_R = 10\text{ }\mu\text{A}$		V_{BR}	5			V
Reverse current	$V_R = 5\text{ V}$		I_R		0.5	10	μA
Capacitance	$V_R = 0\text{ V}$, $f = 1\text{ MHz}$		C_O		30		pF
Temperature coefficient, forward voltage	$I_F = 16\text{ mA}$		$\Delta V_F / \Delta T_A$		- 1.7		mV/ $^{\circ}\text{C}$
OUTPUT							
Logic low supply current	$I_F = 16\text{ mA}$, $V_O = \text{open}$, $V_{CC} = 15\text{ V}$		I_{CCL}		150		μA
Logic high supply current	$I_F = 0\text{ mA}$, $V_O = \text{open}$, $V_{CC} = 15\text{ V}$		I_{CCH}		0.01	1	μA
Output voltage, output low	$I_F = 16\text{ mA}$, $I_O = 1.1\text{ mA}$, $V_{CC} = 4.5\text{ V}$	6N135	V_{OL}		0.1	0.4	V
	$I_F = 16\text{ mA}$, $I_O = 2.4\text{ mA}$, $V_{CC} = 4.5\text{ V}$	6N136	V_{OL}		0.1	0.4	V
Output current, output high	$I_F = 0\text{ mA}$, $V_O = V_{CC} = 5.5\text{ V}$		I_{OH}		3	500	nA
	$I_F = 0\text{ mA}$, $V_O = V_{CC} = 15\text{ V}$		I_{OH}		0.01	1	μA
COUPLER							
Capacitance (input to output)	$f = 1\text{ MHz}$		C_{IO}		0.6		pF

Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

**CURRENT TRANSFER RATIO** ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_F = 16\text{ mA}$, $V_O = 0.4\text{ V}$, $V_{CC} = 4.5\text{ V}$	6N135	CTR	7	16		%
		6N136	CTR	19	35		%
	$I_F = 16\text{ mA}$, $V_O = 0.5\text{ V}$, $V_{CC} = 4.5\text{ V}$	6N135	CTR	5			%
		6N136	CTR	15			%

SWITCHING CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
High to low	$I_F = 16\text{ mA}$, $V_{CC} = 5\text{ V}$, $R_L = 4.1\text{ k}\Omega$	6N135	t_{PHL}		0.3	1.5	μs
	$I_F = 16\text{ mA}$, $V_{CC} = 5\text{ V}$, $R_L = 1.9\text{ k}\Omega$	6N136	t_{PHL}		0.2	0.8	μs
Low to high	$I_F = 16\text{ mA}$, $V_{CC} = 5\text{ V}$, $R_L = 4.1\text{ k}\Omega$	6N135	t_{PLH}		0.3	1.5	μs
	$I_F = 16\text{ mA}$, $V_{CC} = 5\text{ V}$, $R_L = 1.9\text{ k}\Omega$	6N136	t_{PLH}		0.2	0.8	μs

COMMON MODE TRANSIENT IMMUNITY ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
High	$I_F = 0\text{ mA}$, $V_{CM} = 10\text{ V}_{P-P}$, $V_{CC} = 5\text{ V}$, $R_L = 4.1\text{ k}\Omega$	6N135	$ CM_H $		1000		$\text{V}/\mu\text{s}$
	$I_F = 0\text{ mA}$, $V_{CM} = 10\text{ V}_{P-P}$, $V_{CC} = 5\text{ V}$, $R_L = 1.9\text{ k}\Omega$	6N136	$ CM_H $		1000		$\text{V}/\mu\text{s}$
Low	$I_F = 16\text{ mA}$, $V_{CM} = 10\text{ V}_{P-P}$, $V_{CC} = 5\text{ V}$, $R_L = 4.1\text{ k}\Omega$	6N135	$ CM_L $		1000		$\text{V}/\mu\text{s}$
	$I_F = 16\text{ mA}$, $V_{CM} = 10\text{ V}_{P-P}$, $V_{CC} = 5\text{ V}$, $R_L = 1.9\text{ k}\Omega$	6N136	$ CM_L $		1000		$\text{V}/\mu\text{s}$

SAFETY AND INSULATION RATINGS

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)				55/100/21		
Comparative tracking index		CTI	175		399	
V_{IOTM}			8000			V
V_{IORM}			890			V
P_{SO}					500	mW
I_{SI}					300	mA
T_{SI}					175	$^{\circ}\text{C}$
Creepage distance	Standard DIP-8		7			mm
Clearance distance	Standard DIP-8		7			mm
Creepage distance	400 mil DIP-8		8			mm
Clearance distance	400 mil DIP-8		8			mm

Note

- According to DIN EN 60747-5-2 (VDE 0884), this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

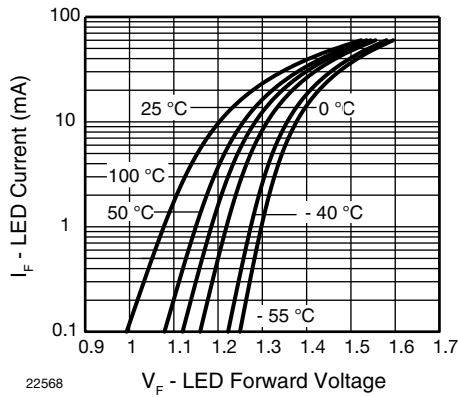
TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)


Fig. 1 - LED Forward Current vs. Forward Voltage

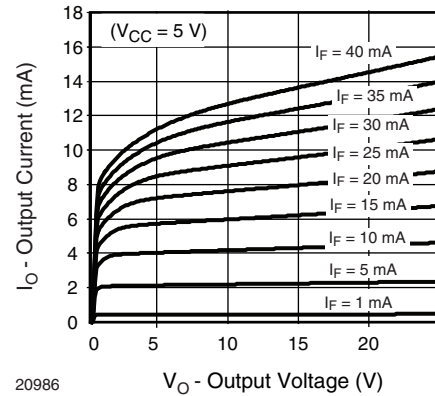


Fig. 4 - Output Current vs. Output Voltage

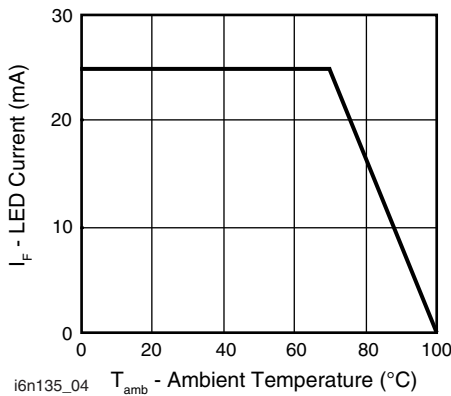


Fig. 2 - Permissible Forward LED Current vs. Temperature

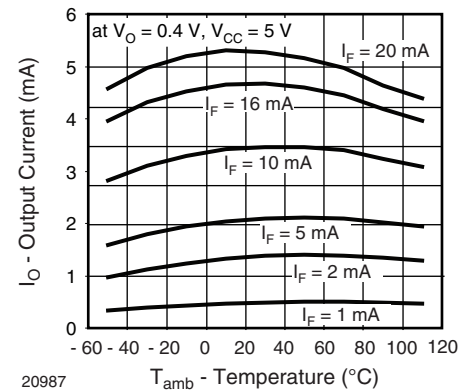


Fig. 5 - Output Current vs. Temperature

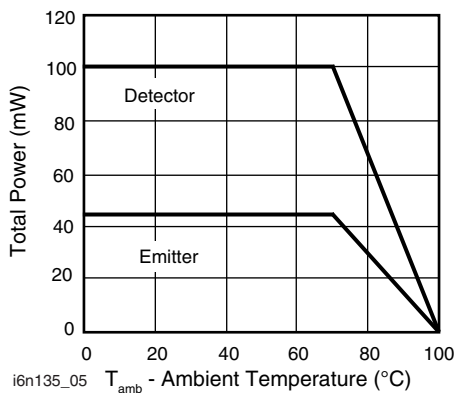


Fig. 3 - Permissible Power Dissipation vs. Temperature

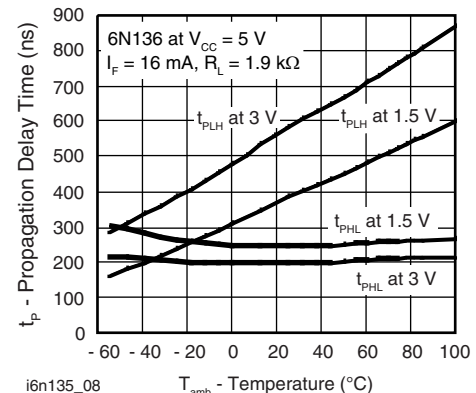


Fig. 6 - Propagation Delay vs. Ambient Temperature

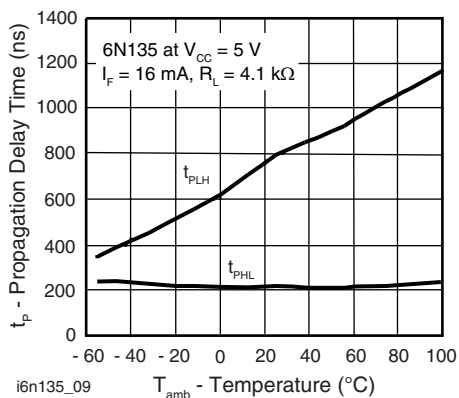


Fig. 7 - Propagation Delay vs. Ambient Temperature

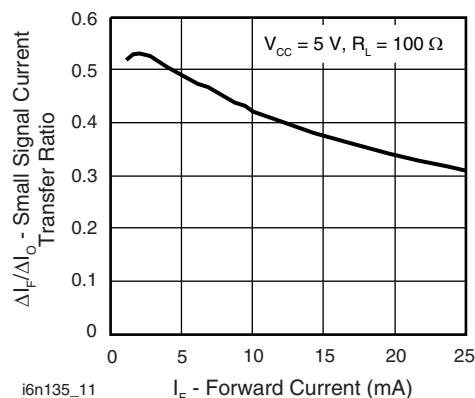


Fig. 9 - Small Signal Current Transfer Ratio vs. Quiescent Input Current

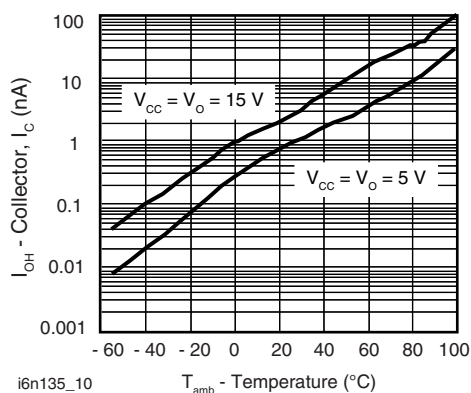


Fig. 8 - Logic High Output Current vs. Temperature

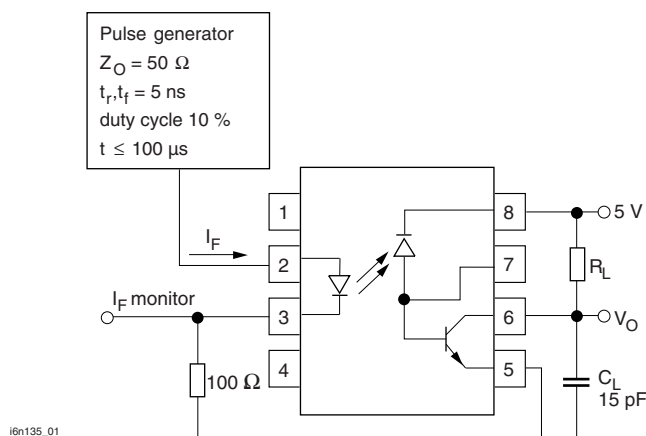
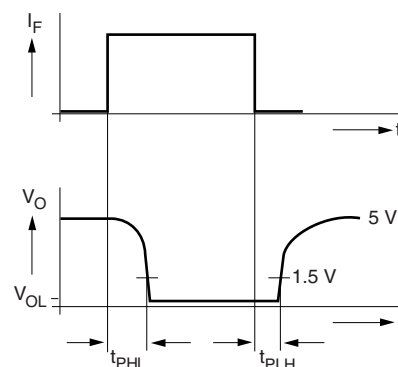


Fig. 10 - Switching Times



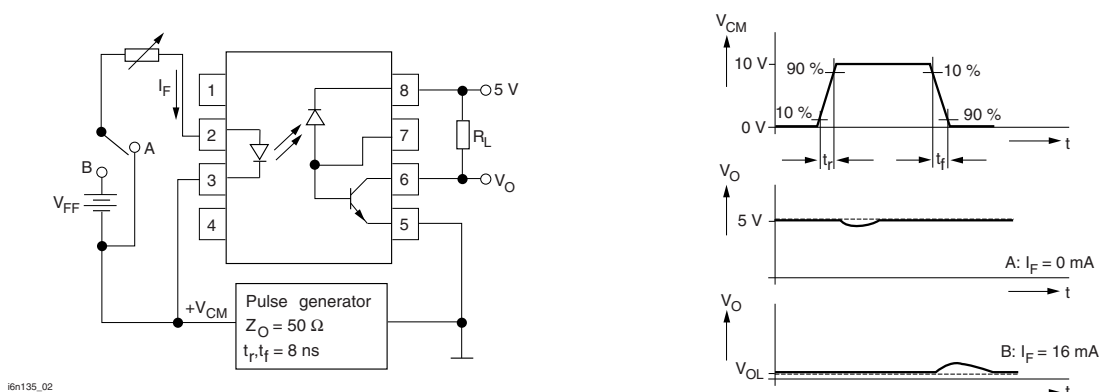
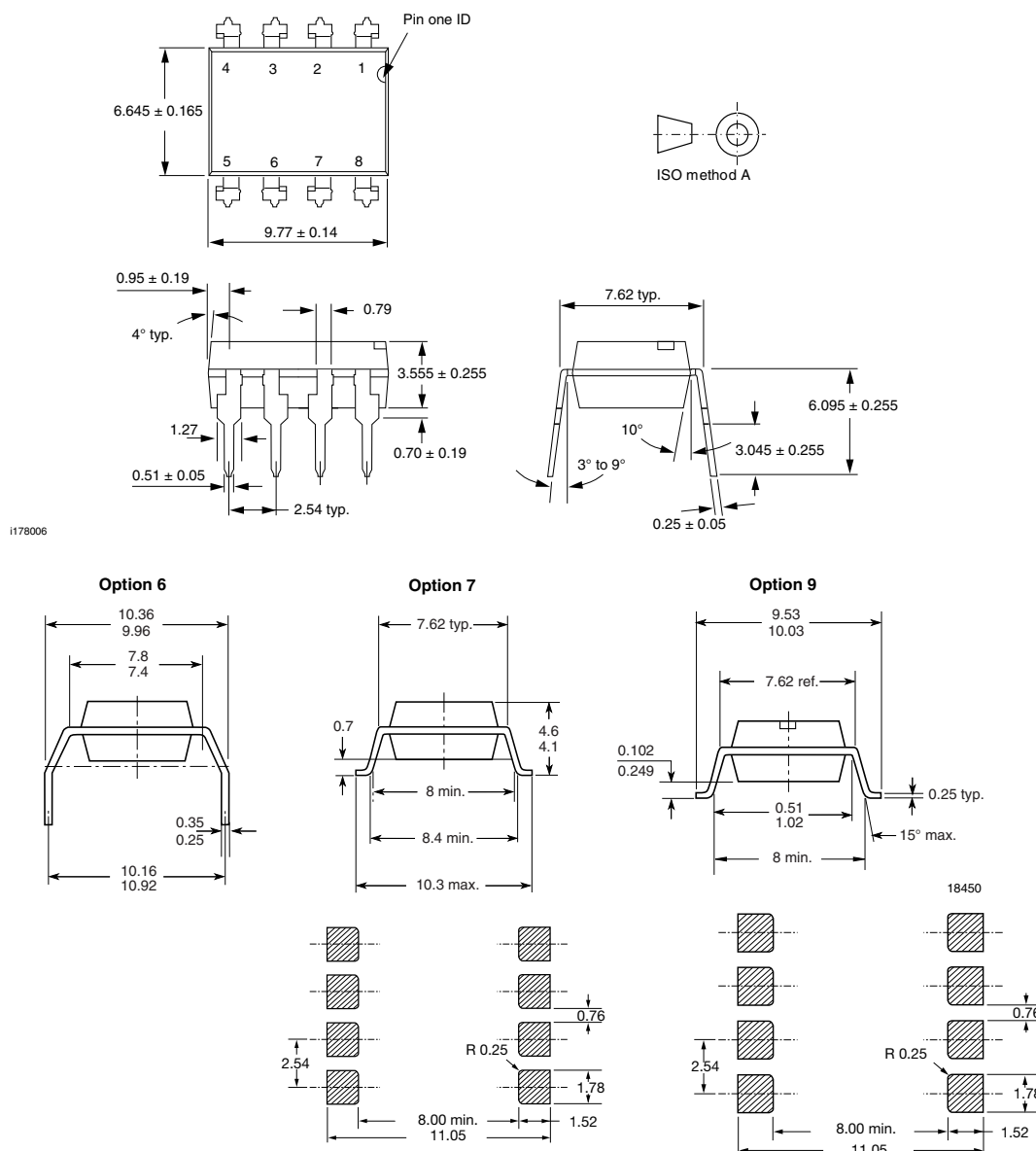


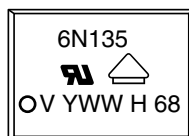
Fig. 11 - Common-Mode Interference Immunity

PACKAGE DIMENSIONS in millimeters





PACKAGE MARKING



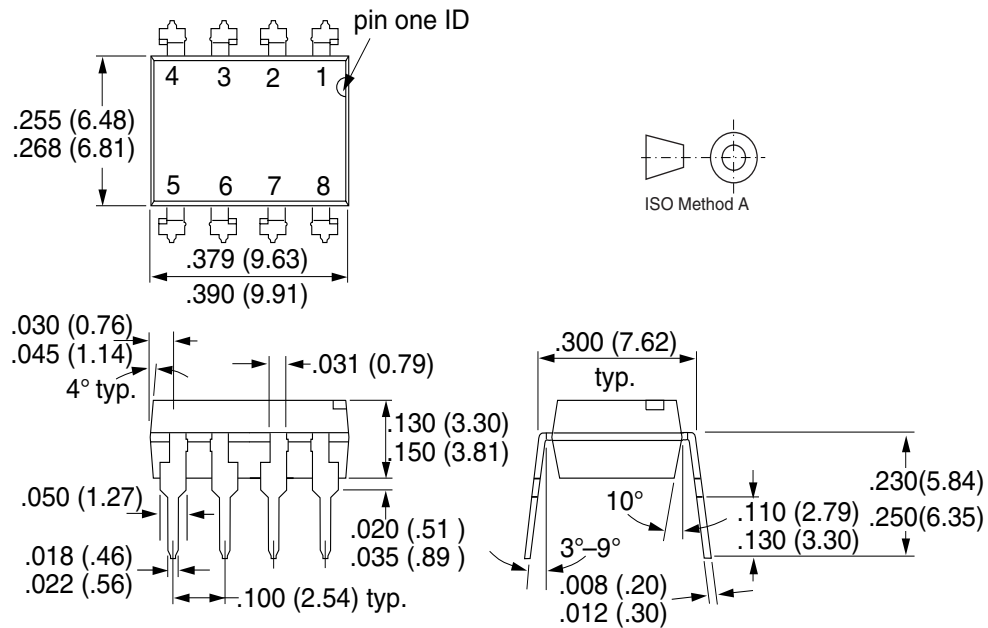
21764-65

Notes

- Only options 1, and 7 are reflected in the package marking.
- The VDE logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.

DIP-8

Package Dimensions in Inches (mm)



i178006



Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

**We reserve the right to make changes to improve technical design
and may do so without further notice.**

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany
Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423

Footprints

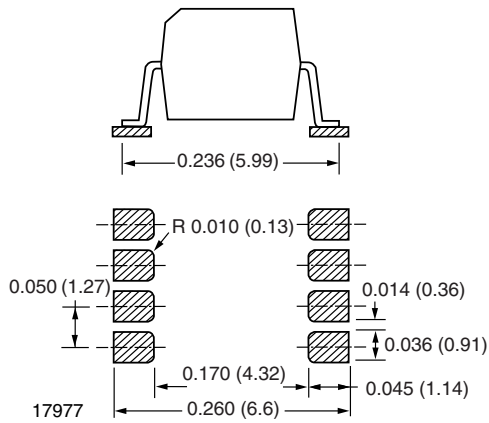


Fig. 1 - SO8A and DSO8A SMD

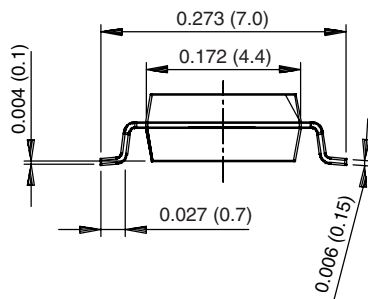


Fig. 2 - SOP-4, Miniflat

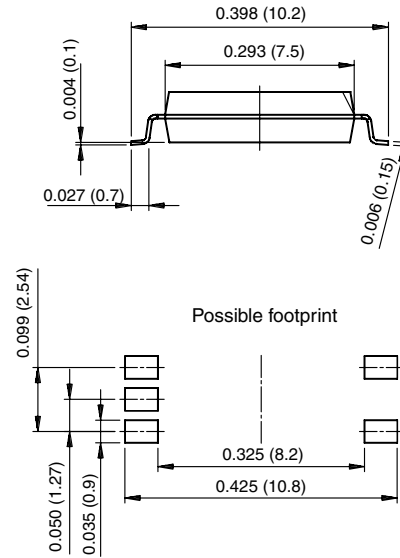


Fig. 3 - SOP-6, 5 Pin Wide Body

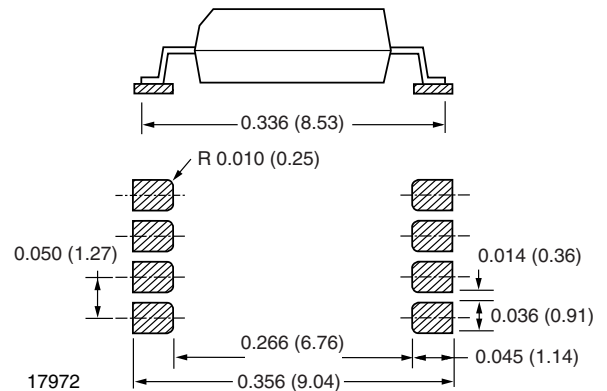


Fig. 4 - 8 Pin PCMCIA

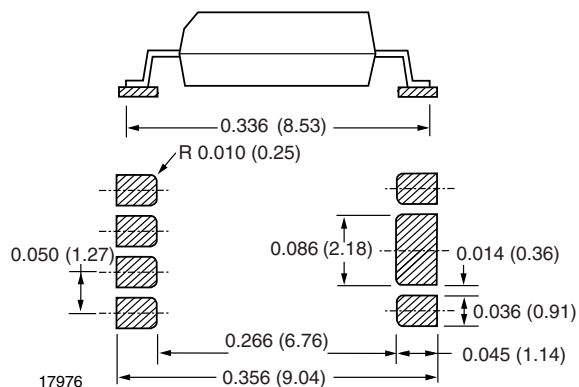


Fig. 5 - 8 Pin PCMCIA, Heat Sink

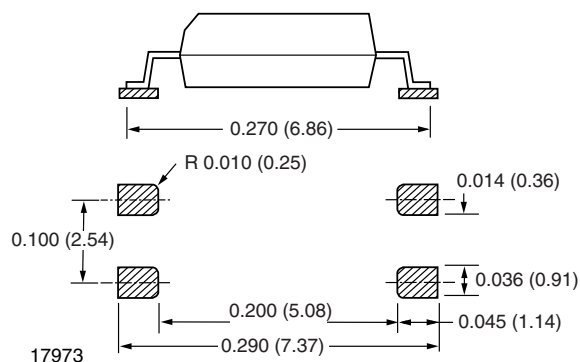


Fig. 8 - 4 Pin Mini-Flat

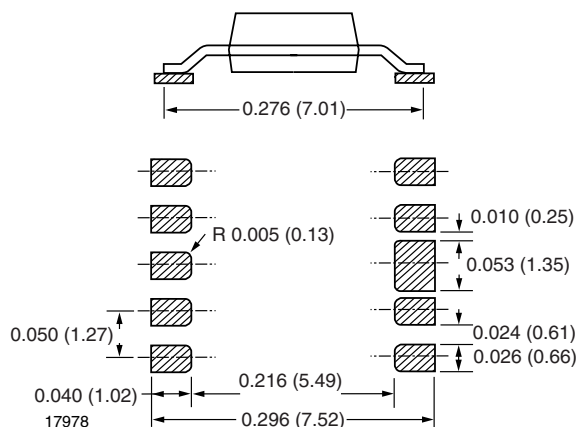


Fig. 6 - Mini Coupler

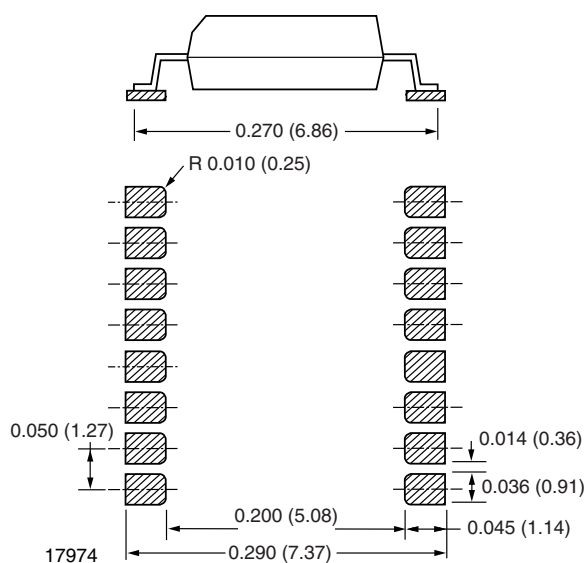


Fig. 7 - SOP-16

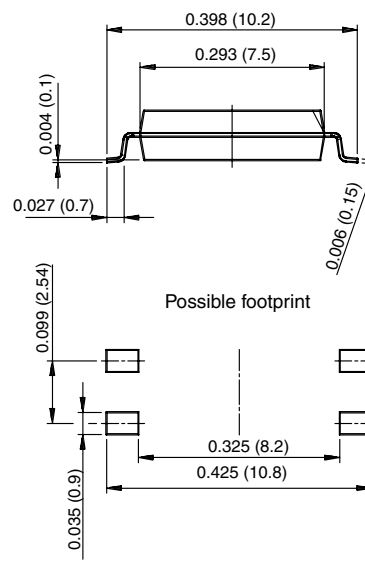


Fig. 9 - SOP-6, 4 Pin Wide Body

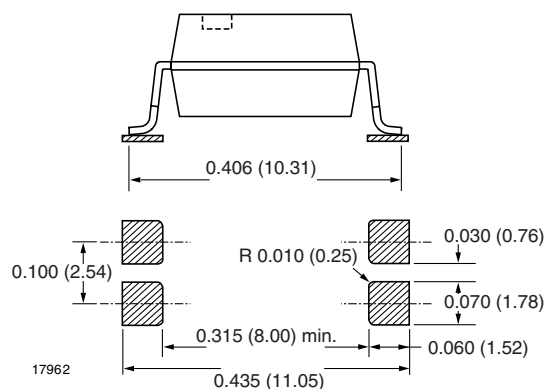


Fig. 10 - 4 Pin SMD Option 7

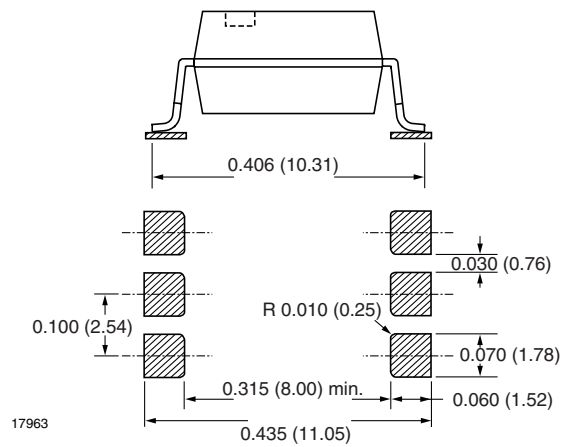


Fig. 11 - 6 Pin SMD Option 7

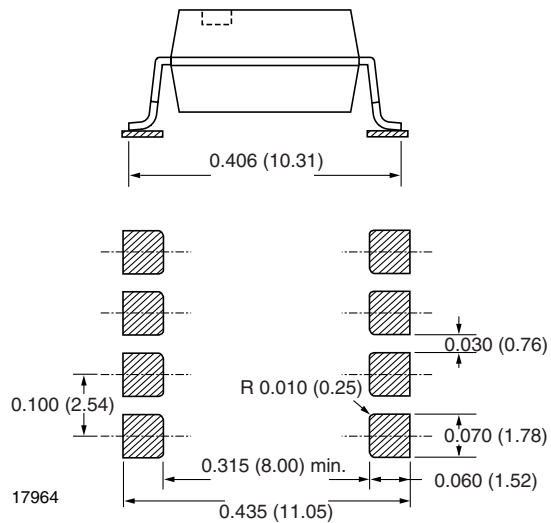


Fig. 12 - 8 Pin SMD Option 7

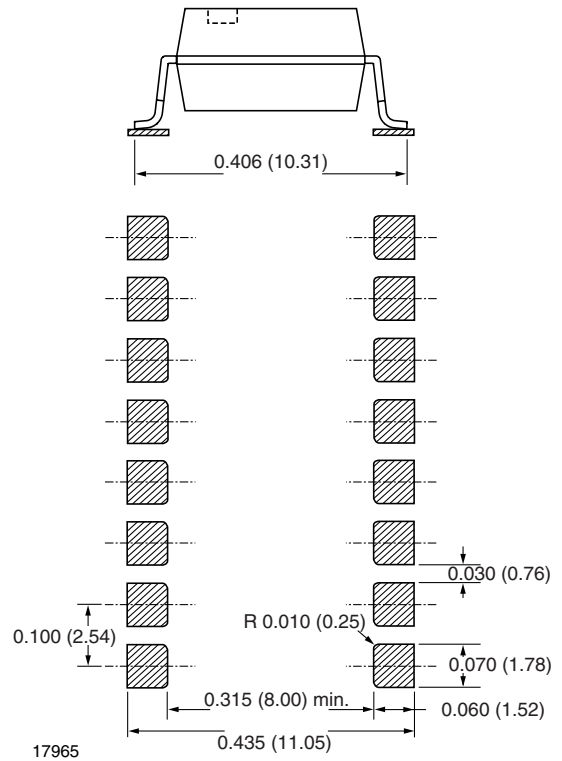


Fig. 13 - 16 Pin SMD Option 7

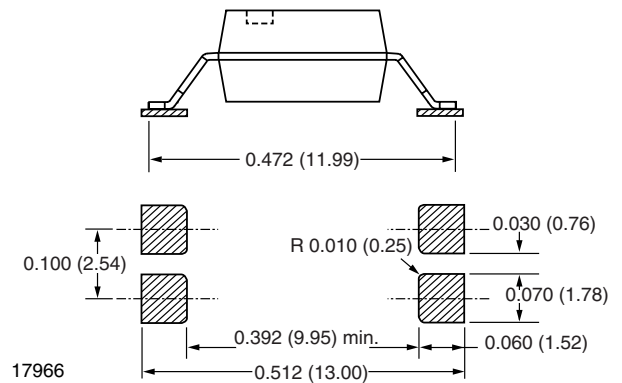


Fig. 14 - 4 Pin SMD Option 8

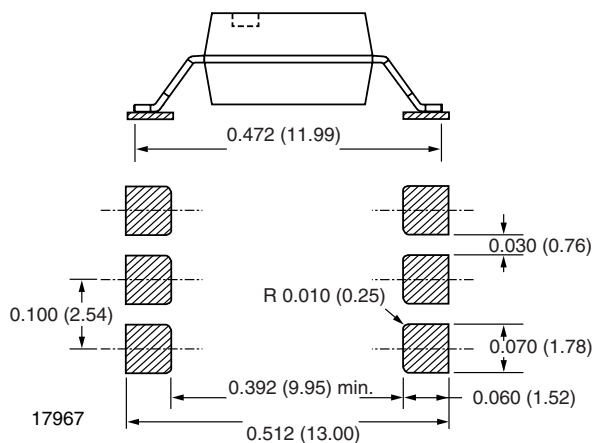


Fig. 15 - 6 Pin SMD Option 8

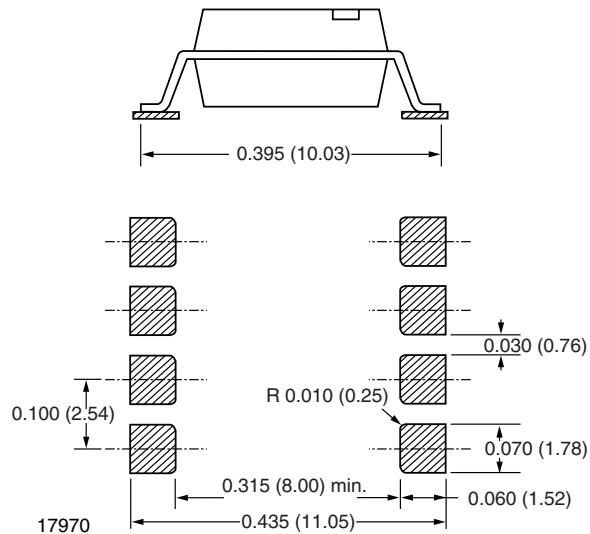


Fig. 18 - 8 Pin SMD Option 9

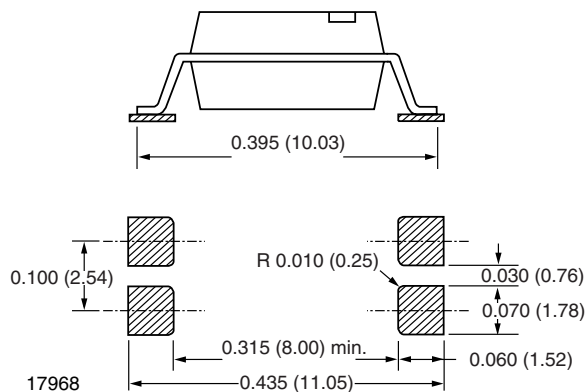


Fig. 16 - 4 Pin SMD Option 9

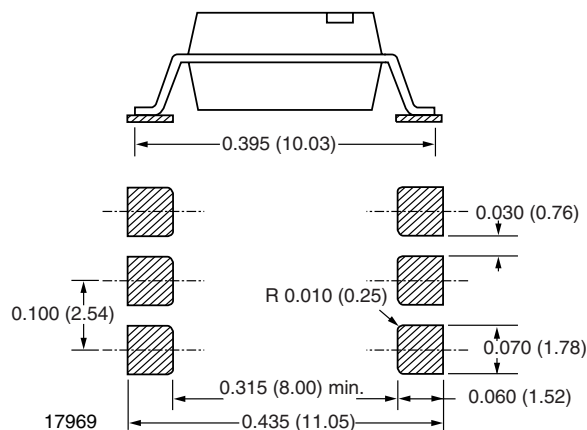


Fig. 17 - 6 Pin SMD Option 9

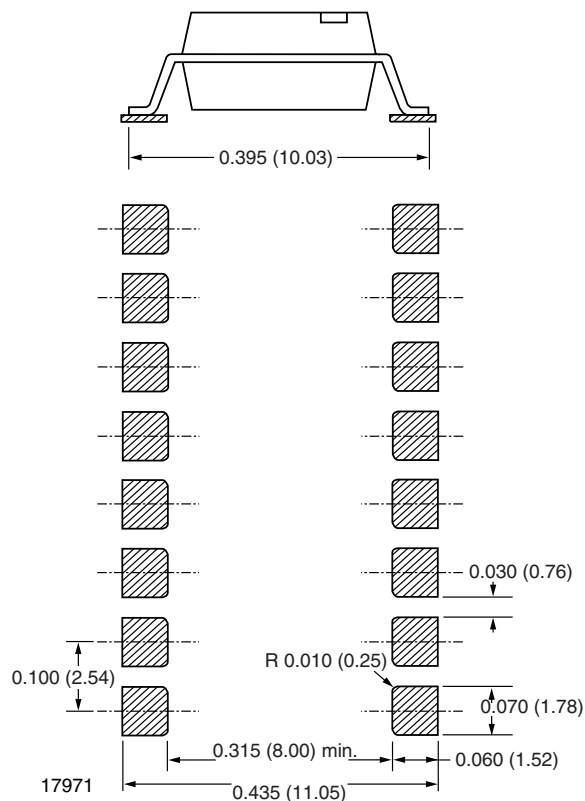


Fig. 19 - 16 Pin SMD Option 9

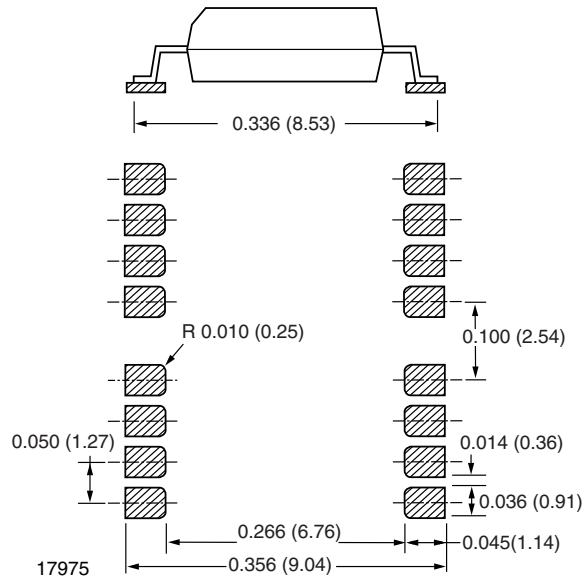


Fig. 20 - 16 Pin PCMCIA



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