



# VERY HIGH SPEED PNP POWER TRANSISTORS

COMPLEMENTARY TO THE D44VH SERIES

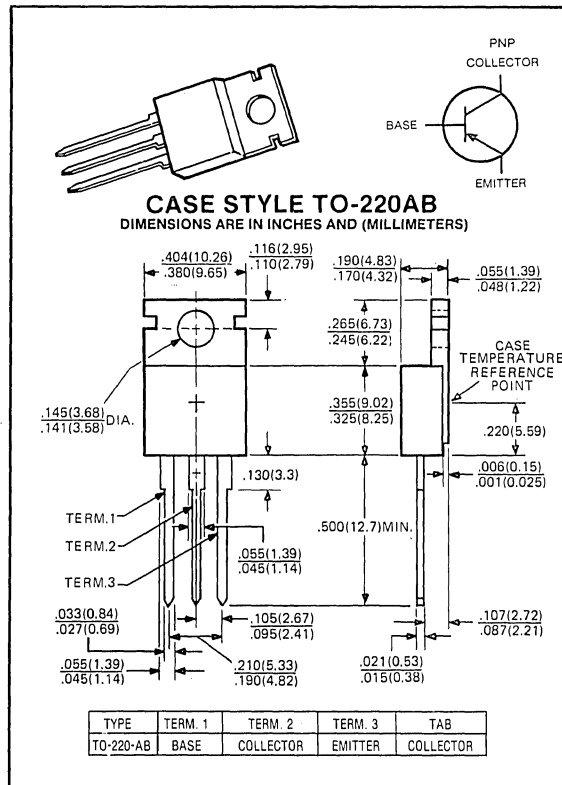
**D45VH Series**

**-30 - -80 VOLTS  
-15 AMP, 83 WATTS**

The D45VH is a PNP power transistor especially designed for use in switching circuits such as switching regulators, high-frequency inverters/converters and other applications where very fast switching and low-saturation voltages are necessary. This device complements the D44VH NPN power transistor and is characterized with performance information which relates directly to switching.

**Features:**

- Fast Switching  $t_s \leq 500$  ns resistive  
 $t_f \leq 100$  ns
- Low  $V_{CE(sat)} \leq 1.0V$  @  $I_C = 8A$



maximum ratings ( $T_A = 25^\circ C$ ) (unless otherwise specified)

| RATING   | SYMBOL         | D45VH1      | D45VH4      | D45VH7      | D45VH10     | UNITS         |
|--|----------------|-------------|-------------|-------------|-------------|---------------|
| Collector-Emitter Voltage                        | $V_{CEO(sus)}$ | -30         | -45         | -60         | -80         | Volts         |
| Collector-Emitter Voltage                        | $V_{CEX}$      | -40         | -55         | -70         | -90         | Volts         |
| Collector-Emitter Voltage                        | $V_{CEV}$      | -50         | -70         | -80         | -100        | Volts         |
| Emitter Base Voltage                             | $V_{EBO}$      | -7          | -7          | -7          | -7          | Volts         |
| Collector Current — Continuous                   | $I_C$          | -15         | -15         | -15         | -15         | A             |
| Peak <sup>(1)</sup>                              | $I_{CM}$       | -20         | -20         | -20         | -20         |               |
| Base Current — Continuous                        | $I_B$          | -5          | -5          | -5          | -5          | A             |
| Peak <sup>(1)</sup>                              | $I_{BM}$       | -10         | -10         | -10         | -10         |               |
| Total Power Dissipation @ $T_c = 25^\circ C$     | $P_D$          | 83          | 83          | 83          | 83          | Watts         |
| @ $T_c = 100^\circ C$                            |                | 33          | 33          | 33          | 33          |               |
| Derate above $25^\circ C$                        |                | .67         | .67         | .67         | .67         | W/ $^\circ C$ |
| Operating and Storage Junction Temperature Range | $T_J, T_{STG}$ | -55 to +150 | -55 to +150 | -55 to +150 | -55 to +150 | $^\circ C$    |

**thermal characteristics**

|  |                 |     |     |     |     |              |
|--|-----------------|-----|-----|-----|-----|--------------|
| Thermal Resistance, Junction to Case   | $R_{\theta JC}$ | 1.5 | 1.5 | 1.5 | 1.5 | $^\circ C/W$ |
| Thermal Resistance, Junction to Ambient                                      | $R_{\theta JA}$ | 75  | 75  | 75  | 75  | $^\circ C/W$ |
| Maximum Lead Temperature for Soldering Purpose: 1/8" from Case for 5 Seconds | $T_L$           | 235 | 235 | 235 | 235 | $^\circ C$   |

(1) Pulse measurement condition  $PW \leq 6.0$  ms, see Figure 14.

electrical characteristics ( $T_C = 25^\circ\text{C}$ ) (unless otherwise specified)

| CHARACTERISTICS  | SYMBOL         | MIN                      | MAX              | UNIT          |
|--|----------------|--------------------------|------------------|---------------|
| Collector-Emitter Sustaining Voltage <sup>(1)</sup> ( $I_C = 100\text{mA}$ , $I_B = 0$ )<br>D45VH1<br>D45VH4<br>D45VH7<br>D45VH10  | $V_{CEO(sus)}$ | -30<br>-45<br>-60<br>-80 | —<br>—<br>—<br>— | V             |
| Collector-Emitter Voltage <sup>(2)</sup><br>( $I_C = 10\text{A}$ , $V_{CLAMP} = \text{Rated } V_{CEX}$ , $T_C = 100^\circ\text{C}$ )<br>D45VH1<br>D45VH4<br>D45VH7<br>D45VH10                | $V_{CEX}$      | -40<br>-55<br>-70<br>-90 | —<br>—<br>—<br>— | V             |
| Collector Cutoff Current<br>( $V_{CEV} = \text{Rated Value}$ , $V_{BE(off)} = 4.0\text{V}$ )<br>( $V_{CEV} = \text{Rated Value}$ , $V_{BE(off)} = 4.0\text{V}$ , $T_C = 100^\circ\text{C}$ ) | $I_{CEV}$      | —<br>—                   | -10<br>-100      | $\mu\text{A}$ |
| Collector Cutoff Current<br>( $V_{CE} = \text{Rated } V_{CEV}$ , $R_{BE} = 50\ \Omega$ , $T_C = 100^\circ\text{C}$ )   | $I_{CER}$      | —                        | -100             | $\mu\text{A}$ |
| Emitter Cutoff Current ( $V_{EB} = -7\text{V}$ , $I_C = 0$ )   | $I_{EBO}$      | —                        | -10              | $\mu\text{A}$ |

second breakdown

|   |            |              |
|---|------------|--------------|
| Second Breakdown with Base Forward Biased | $F_{BSOA}$ | SEE FIGURE 7 |
| Second Breakdown with Base Reverse Biased | $R_{BSOA}$ | SEE FIGURE 8 |

on characteristics<sup>(1)</sup>

|  |               |             |                      |   |
|--|---------------|-------------|----------------------|---|
| DC Current Gain<br>( $I_C = -2\text{A}$ , $V_{CE} = -1\text{V}$ )<br>( $I_C = -4\text{A}$ , $V_{CE} = -1\text{V}$ )  | $h_{FE}$      | 35<br>20    | —<br>—               | — |
| Collector-Emitter Saturation Voltage<br>( $I_C = -8\text{A}$ , $I_B = -0.8\text{A}$ )<br>( $I_C = -8\text{A}$ , $I_B = -0.8\text{A}$ , $T_C = 100^\circ\text{C}$ )<br>( $I_C = -15\text{A}$ , $I_B = -3.0\text{A}$ , $T_C = 100^\circ\text{C}$ ) | $V_{CE(sat)}$ | —<br>—<br>— | -1.0<br>-1.1<br>-1.5 | V |
| Base-Emitter Saturation Voltage<br>( $I_C = -8\text{A}$ , $I_B = -0.8\text{A}$ )<br>( $I_C = -8\text{A}$ , $I_B = -0.8\text{A}$ , $T_C = 100^\circ\text{C}$ )  | $V_{BE(sat)}$ | —<br>—      | -1.4<br>-1.4         | V |

dynamic characteristics

Typical

|  |          |     |     |
|--|----------|-----|-----|
| Current-Gain — Bandwidth Product<br>( $I_C = -0.1\text{A}$ , $V_{CE} = -10\text{V}$ , $f_{test} = 1\text{MHz}$ ) | $f_T$    | 50  | MHz |
| Output Capacitance<br>( $V_{CB} = -10\text{V}$ , $I_E = 0$ , $f_{test} = 1\text{MHz}$ )                          | $C_{OB}$ | 275 | PF  |

switching characteristics

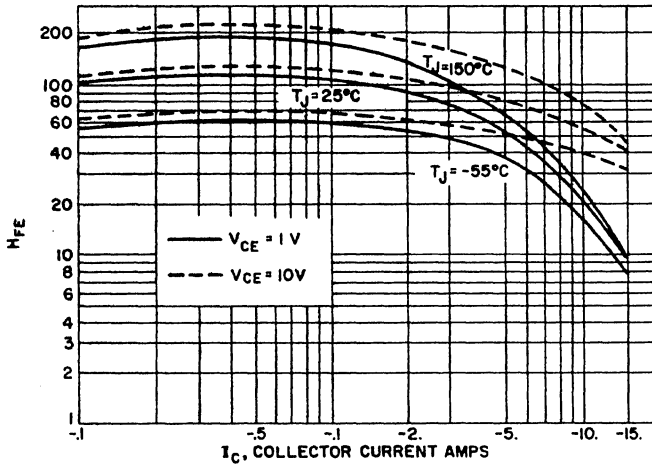
Maximum

| Resistive Load (See Figure 16 for Test Circuit)          |   | $T_C$ | 25°C           | 100°C |      |
|--|---|-------|----------------|-------|------|
| Delay Time   | $V_{CC} = -20\text{V}$ , $I_C = -8\text{A}$<br>$I_{B1} = I_{B2} = -0.8\text{A}$<br>$t_p = 25\ \mu\text{sec}$                              | $t_d$ | 50             | —     | nsec |
| Rise Time  |   | $t_r$ | 250            | —     | nsec |
| Storage Time   |   | $t_s$ | 500            | —     | nsec |
| Fall Time  |   | $t_f$ | 100            | —     | nsec |
| Inductive Load, Clamped (See Figure 15 for Test Circuit) |   |       |                |       |      |
| Storage Time   | $V_{CC} = -20\text{V}$ , $I_C = -8\text{A}$<br>$V_{CLAMP} = \text{Rated } V_{CEX}$<br>$I_{B1} = 0.8\text{A}$ , $V_{BE(off)} = -5\text{V}$ | $t_s$ | 500            | 600   | nsec |
| Fall Time  |   | $t_f$ | 300            | 400   | nsec |
|  |   |       | <b>Typical</b> |       |      |
| Storage Time   | $L = 200\ \mu\text{h}$  | $t_s$ | 200            | 320   | nsec |
| Fall Time  |   | $t_f$ | 160            | 180   | nsec |

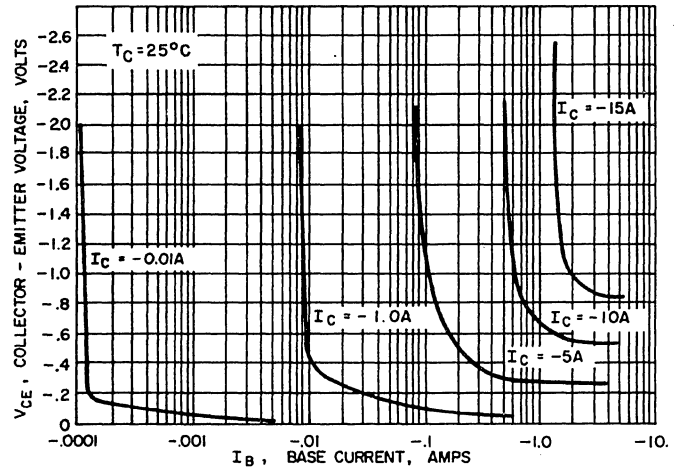
(1) Pulse Duration = 300  $\mu\text{sec}$ , Duty Factor  $\leq 2\%$ .

(2) See Figure 15 for Test Circuit.

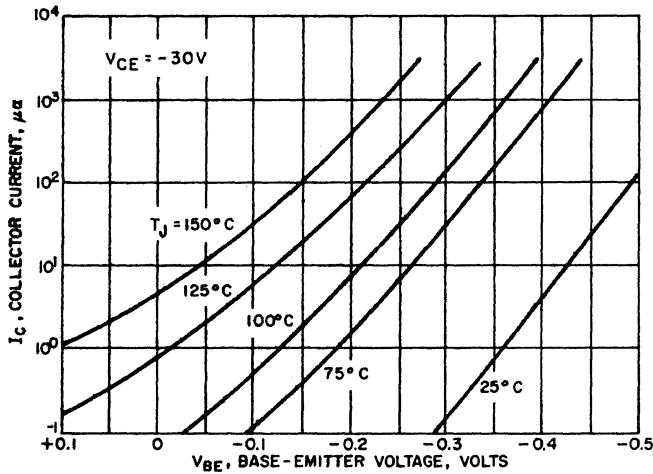
# TYPICAL DC CHARACTERISTICS



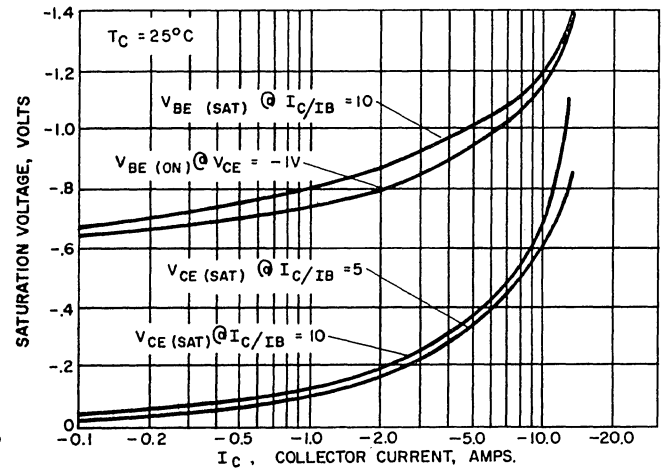
1. DC CURRENT GAIN



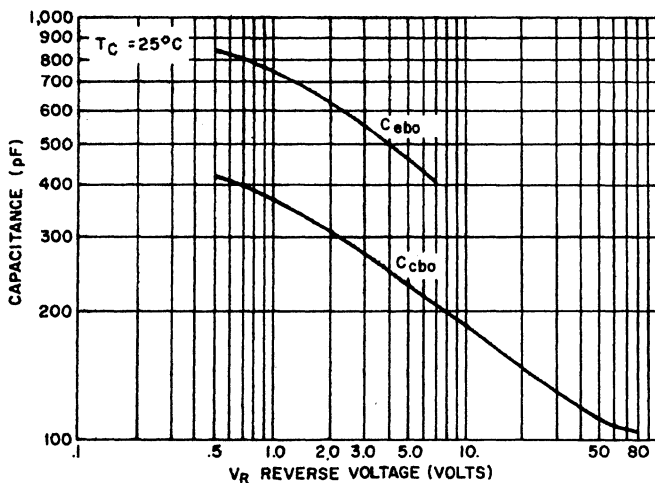
2. COLLECTOR SATURATION REGION



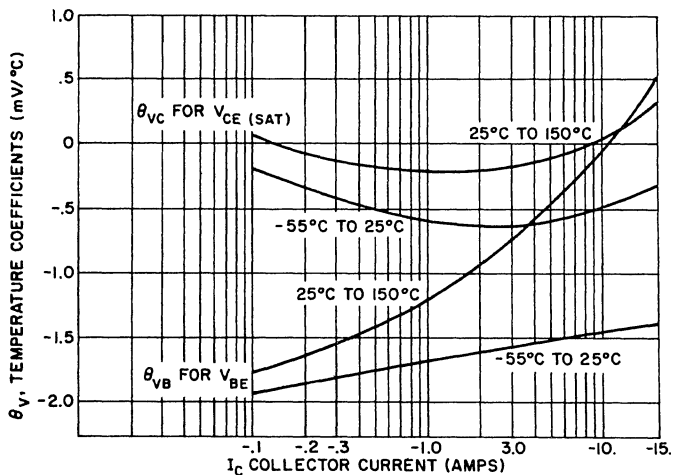
3. COLLECTOR CUTOFF REGION



4. SATURATION VOLTAGE

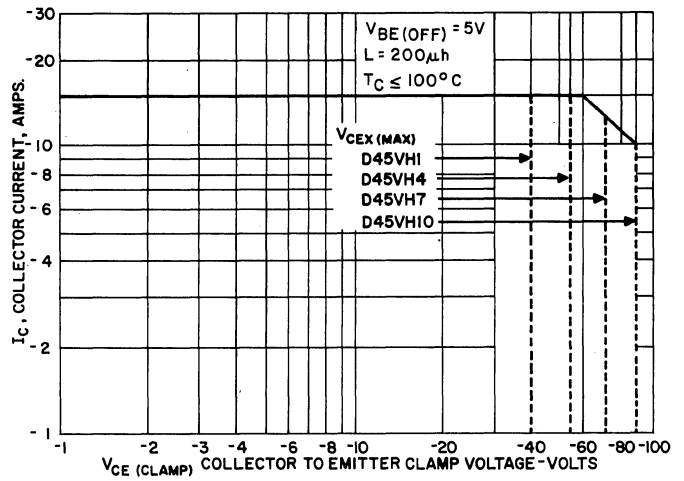
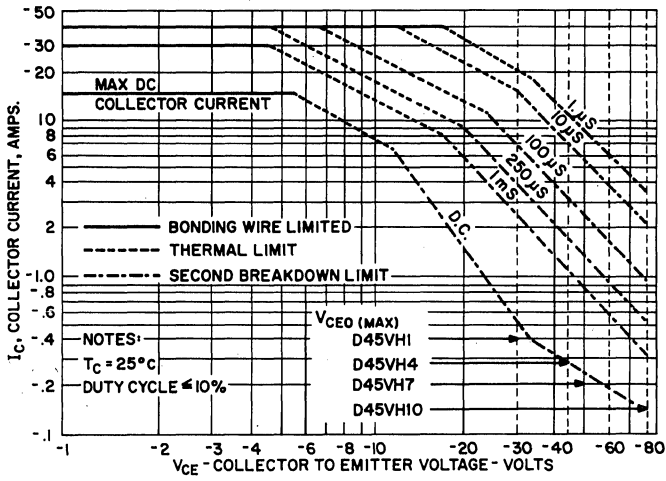


5. CAPACITANCE

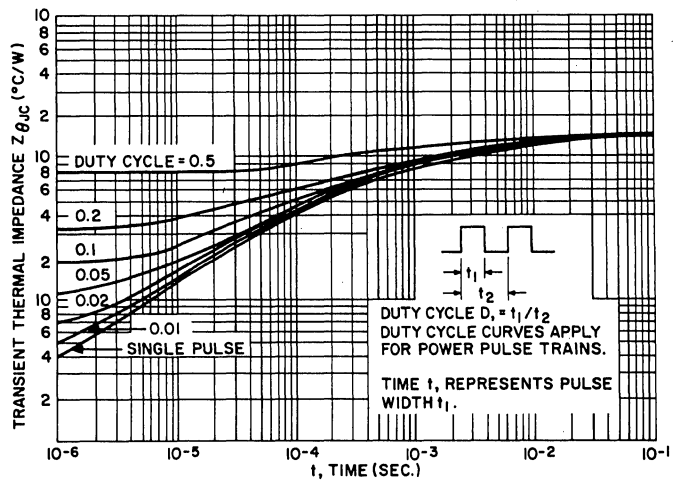
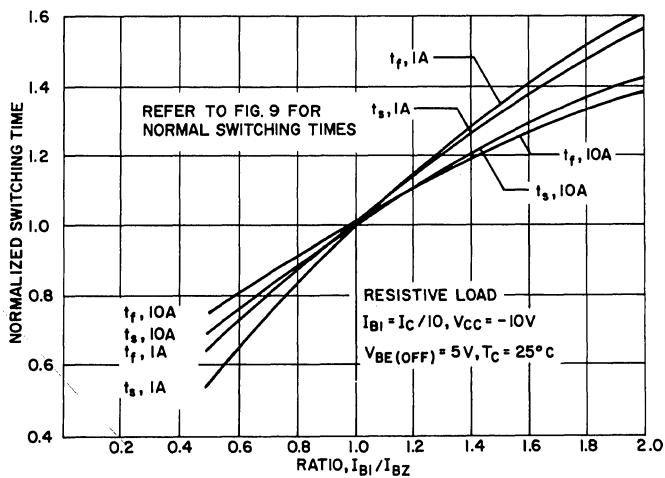
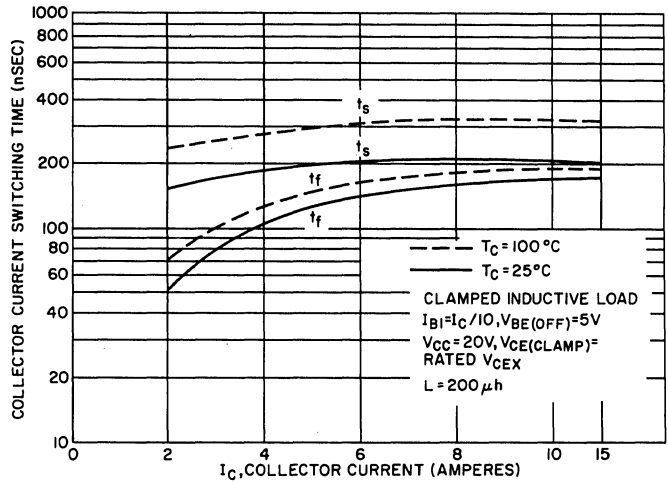
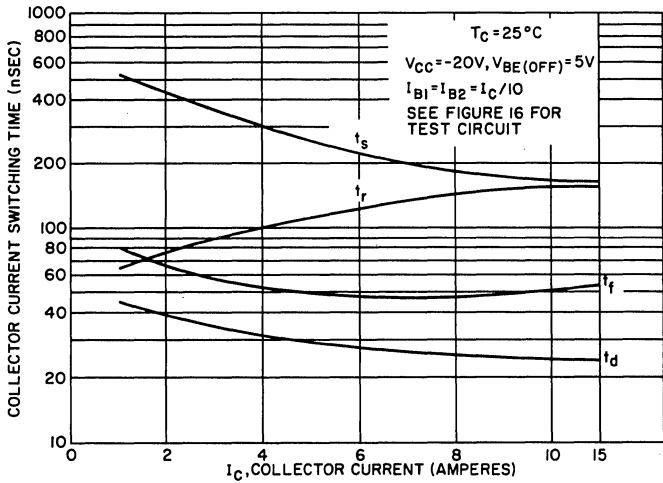


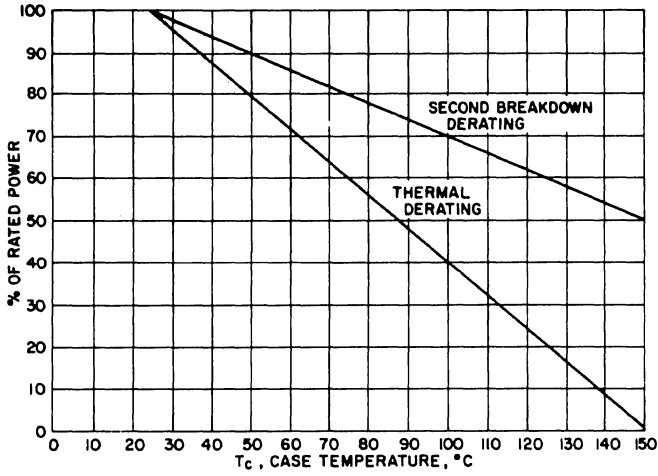
6. SATURATION VOLTAGE TEMPERATURE COEFFICIENTS

## SAFE OPERATING AREA

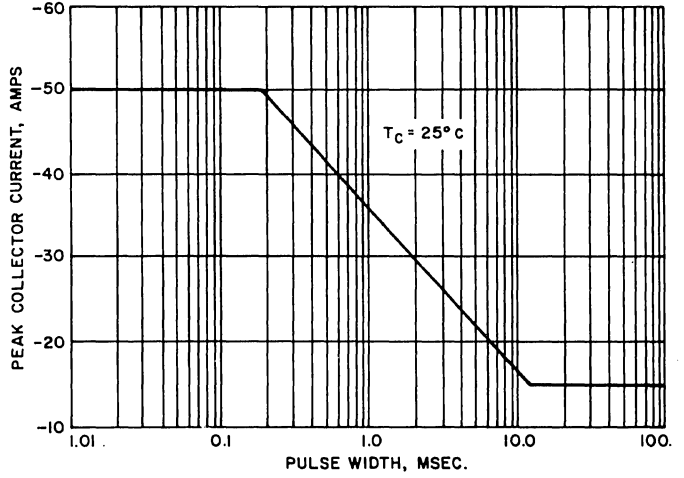


## TYPICAL SWITCHING CHARACTERISTICS



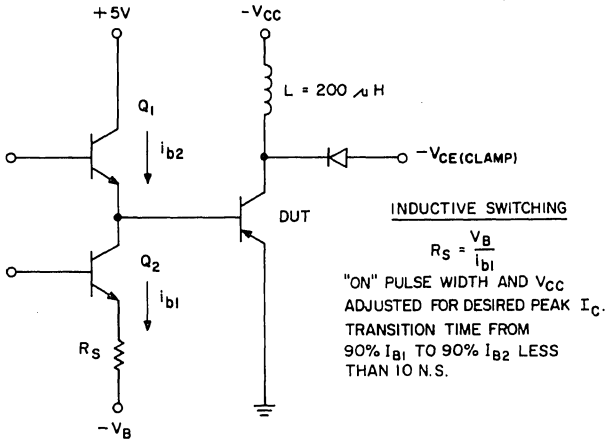


13. POWER DERATING FACTOR

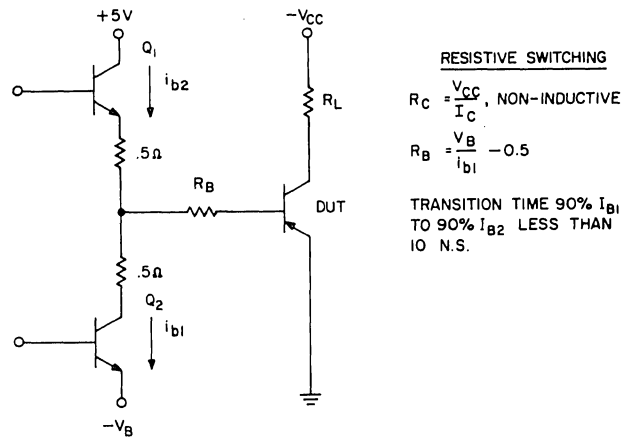


14. MAXIMUM SINGLE PULSE COLLECTOR CURRENT

TEST CIRCUITS



15. INDUCTIVE SWITCHING AND  $V_{CEX}$



16. RESISTIVE SWITCHING